

DRD870G40

Rectifier Diode

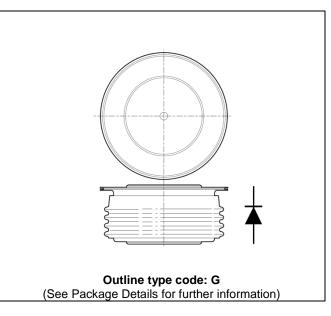
DS5986-1 January 2011 (LN28007)

FEATURES

- Double Side Cooling
- High Surge Capability

KEY PARAMETERS

V _{RRM}	4000V
I _{F(AV)}	870A
I _{FSM}	15000A



VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{RRM} V	Conditions
DRD870G40 DRD870G38 DRD870G36 DRD870G34	4000 3800 3600 3400	V _{RSM} = V _{RRM} +100V

Fig. 1 Package outline

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD870G36 for a 3600V device

CURRENT RATINGS

 $T_{case} = 75^{\circ}C$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Si	de Cooled		I	
I _{F(AV)}	Mean forward current	Half wave resistive load	1121	А
I _{F(RMS)}	RMS value	-	1761	А
l _F	Continuous (direct) on-state current	-	1608	А
Single Sic	le Cooled (Anode side)		I	
I _{F(AV)}	Mean forward current	Half wave resistive load	734	А
I _{F(RMS)}	RMS value	-	1154	А
I _F	Continuous (direct) on-state current	-	989	А

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Si	de Cooled		•	
I _{F(AV)}	Mean forward current	Half wave resistive load	870	А
I _{F(RMS)}	RMS value	-	1366	А
I _F	Continuous (direct) on-state current	-	1280	А
Single Sic	le Cooled (Anode side)	·		
I _{F(AV)}	Mean forward current	Half wave resistive load	550	А
I _{F(RMS)}	RMS value	-	863	А
l _F	Continuous (direct) on-state current	-	740	А

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	12	kA
l ² t	I ² t for fusing	$V_R = 50\% V_{RRM}$ - ¼ sine	0.72	MA ² s
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	15	kA
l ² t	I ² t for fusing	V _R = 0	1.125	MA ² s

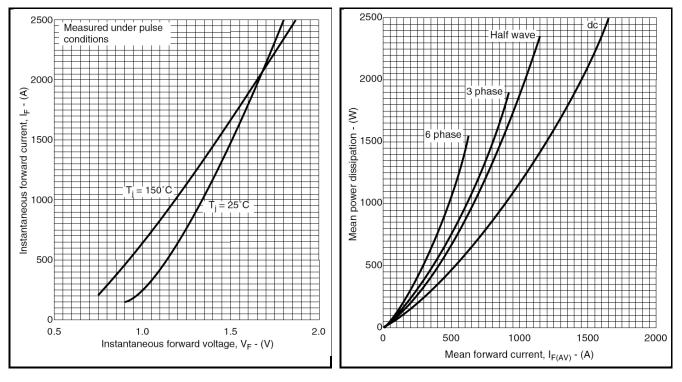
THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Condition	S	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.032	°C/W
		Single side cooled	Anode DC	-	0.064	°C/W
			Cathode DC	-	0.064	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Clamping force 12kN	Double side	-	0.008	°C/W
		(with mounting compound)	Single side	-	0.016	°C/W
T _{vj}	Virtual junction temperature	On-state (conducting)		-	160	°C
		Reverse (blocking)		-	150	°C
T _{stg}	Storage temperature range			-55	175	°C
Fm	Clamping force			11.5	13.5	kN

CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V_{FM}	Forward voltage	At 1800A peak, T _{case} = 25°C	-	1.6	V
I _{RM}	Peak reverse current	At V _{RRM} , T _{case} = 150°C	-	50	mA
Q_S	Total stored charge	I _F = 1000A, dI _{RR} /dt =3A/μs	-	2000	μC
Irr	Peak reverse recovery current	$T_{case} = 150^{\circ}C, V_{R} = 100V$	-	80	А
V_{TO}	Threshold voltage	At T _{vj} = 150°C	-	0.75	V
r _T	Slope resistance	At T _{vj} = 150°C	-	0.44	mΩ

CURVES





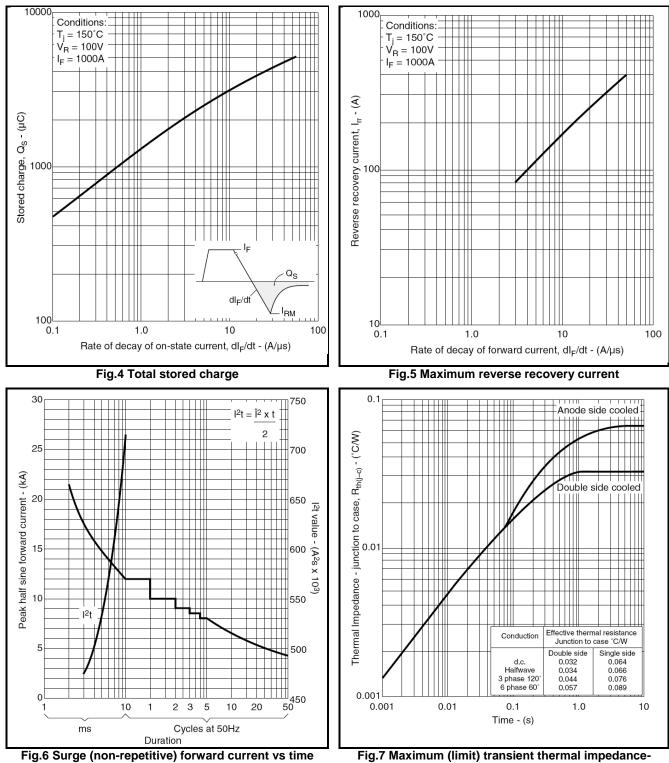
V_{TM} EQUATION

 $V_{TM} = A + BIn (I_T) + C.I_T + D.\sqrt{I_T}$

Fig.3 Dissipation curves

Where A = 0.616461 B = -0.01452 C = 0.000349 D = 0.009952 these values are valid for T_{i} = 150°C for I_{F} 500A to 2500A

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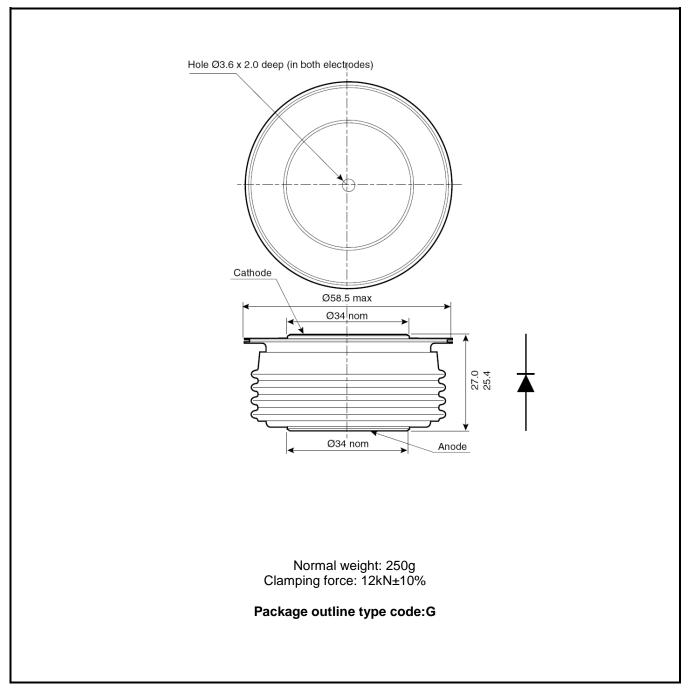


(with 50% V_{RRM} at T_{case} 150°C)

g.7 Maximum (limit) transient thermal impedance junction to case

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Note:

Some packages may be supplied with gate and or tags.

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